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EDUCATIONS

- **B.S., Electronic Engineering** Mar. 2005 – Jan. 2012
Korea Advanced Institute of Science and Technology (KAIST), Daejeon, South Korea
- **M.S., Electrical Engineering** Feb. 2012 – Feb. 2014
Korea Advanced Institute of Science and Technology (KAIST), Daejeon, South Korea
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Thesis: Atomic Layer Deposition of Workfunction-tunable TiC and Nickel Films
- **Ph.D., Electrical Engineering** Mar. 2014 – Present
Korea Advanced Institute of Science and Technology (KAIST), Daejeon, South Korea
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EXPERIENCES

- Public service worker as military affairs in a Cheongju school for the blind, Jan. 2008 – Feb. 2010
- Internship in SK Hynix, Dec. 2012 – Jan. 2013

INTERNATIONAL & DOMESTIC CONFERENCES

- 2015 International Conference on Electronics, Information, and Communication (ICEIC), Chan Hak Yu[†], **Choong-Ki Kim**[†], Jae Hur, Yang-Kyu Choi, and Sung-Yool Choi, “*Low-Frequency Noise in Hysteresis-Free Multilayer MoS₂ FETs*” († equally contribution)
- 2017 International Conference on Electronics, Information, and Communication (ICEIC), Jae Hur[†], **Choong-Ki Kim**[†], Da-Jin Kim, Tewook Bang, and Yang-Kyu Choi, “*Silicon-Based Yagi-Uda Antenna Reflector*” († equally contribution)
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- 2015 ICAMD, Gwang Hyuk Shin, **Choong-Ki Kim**, Gyeong Sook Bang, Jong Yun Kim, Byung Chul Jang, Beom Jun Koo, Myung Hun Woo, Sung-Yool Choi, “*Multilevel resistive switching memory using two dimensional (2D) layered materials*”
- 2016 International Conference on Electronics, Information, and Communication (ICEIC), Da-Jin kim[†], Tewook Bang[†], Jae Hur, **Choong-Ki Kim**, and Yang-Kyu Choi, “*Optimization of the Intrinsic Length of a PIN Diode for a Reconfigurable Antenna*” († equally contribution)
- 2016 International Electron Device Meeting (IEDM), *IEEE*, Dong-II Moon, Jun-Young Park, Jin-Woo Han, Gwang-Jae Jeon, Ji-Yeon Kim, Joon-Bae Moon, Myeong-Lok Seol, **Choong-Ki. Kim**, Han Cheol Lee, M. Meyyappan, and Yang-Kyu Choi, “*Sustainable Electronics for Nano-Spacecraft in Deep Space Missions*”
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- 2015 한국반도체학술대회, Tewook Bang, Hagyoul Bae, **Choong-Ki Kim**, Jun-Young Park, Dae-Chul Ahn, Gun-Hee Kim, Yun-Ik Son, Jae-Hoon Lee, Yong-Taik Kim, and Yang-Kyu Choi, “Improved Split C-V Technique for Accurate Extraction of Mobility by Considering Effective Inversion Charges in p-Channel Si_{0.8}Ge_{0.2} MOSFET”
- 2015 한국반도체학술대회, Tewook Bang, Ui-Sik Jeong, **Choong-Ki Kim**, Hagyoul Bae, Gun-Hee Kim, Da-Jin Kim and Yang-Kyu Choi, “Nanowire Width-Dependent Low-Frequency Noise Characteristics in SiNW-based GAA JL Flash Memory with Extremely Small Dimension”
- 2015 한국반도체학술대회, Gun-Hee Kim, Hagyoul Bae, Yong-Yoon Kim, **Choong-Ki Kim**, Tewook Bang, Yoon-Ik Son, and Yang-Kyu Choi, “Separate Extraction of Source and Drain Resistances using Double Sweep Saturation Current-Voltage Characteristic in SiGe pMOSFET”

INTERNATIONAL PAPERS

- **Choong-Ki Kim**, Hyunjun Ahn, Jung Min Moon, Sukwon Lee, Dong-Il Moon, Jeong Soo Park, Byung-Jin Cho, Yang-Kyu Choi, Seok-Hee Lee, “Temperature Control for the Gate Workfunction Engineering of TiC Film by Atomic Layer Deposition”, Solid State Electronics, 15.08.29 published
- Hagyoul Bae[†], **Choong-Ki Kim**[†], Seung-Bae Jeon, GwangHyuk Shin, EungTaek Kim, Jeong-Gyu Song, Youngjun Kim, Hyungjun Kim, Sung-Yool Choi, Kyung Cheol Choi, and Yang-Kyu Choi, “A Separate Extraction Method for Asymmetric Source and Drain Resistances Using Frequency-Dispersive C-V Characteristics in Exfoliated MoS₂ FET”, Electron Device Letter, *IEEE*, 16.01.08 published († equally contribution)
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- Ui-sik Jeong[†], **Choong-Ki Kim**[†], Hagyoul Bae, Dong-Il Moon, Tewook Bang, Ji-Min Choi, Jae Hur, and Yang-Kyu Choi, “Investigation of Low-Frequency Noise in Nonvolatile Memory Composed of a Gate-All-Around Junction”, Transaction on Electron Device, *IEEE*, 16.03.28 published († equally contribution)
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- **Choong-Ki Kim**[†], Eungtaek Kim[†], Myung Keun Lee, Jun-Young Park, Myeong-Lok Seol, Hagyoul Bae, Tewook Bang, Seung-Bae Jeon, Sungwoo Jun, Sang-hee K. Park, Kyung cheol Choi, and Yang-Kyu Choi, “Electrothermal Annealing (ETA) Method to Enhance the Electrical Performance of Amorphous-Oxide-Semiconductor (AOS) Thin-Film Transistors (TFTs)”, ACS Applied Materials and Interfaces, 2016.08.23 published († equally contribution)
- **Choong-Ki Kim**[†], Eun Gyo Jeong[‡], Eungtaek Kim, Jeong-Gyu Song, Youngjun Kim, Whang Je Woo, Myung Keun Lee, Hagyoul Bae, Seong-Bae Jeon, Hyungjun Kim, Kyung Cheol Choi, and Yang-Kyu Choi, “Highly Stable 2D Material (2DM) Field-Effect-Transistors (FETs) with Wafer-Scale Multi-dyad Encapsulation”, Nanotechnology, 2016.12.28 published († equally contribution)
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• Jungmin Moon, Hyun Jun Ahn, Yujin Seo, Tae In Lee, **Choong-Ki Kim**, Il Cheol Rho, Choon Hwan Kim, Wan Sik Hwang, and Byung Jin Cho, “The Work Function Behavior of Aluminum-doped Titanium-Carbide Grown by Atomic Layer Deposition”, Transaction on Electron Device, *IEEE*, 16.02.24 published

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• Myung Keun Lee[†], **Choong-Ki Kim**[†], Eungtaek Kim, Jeong Woo Park, Myeong-Lok Seol, Jun-Young Park, Sang-Hee Ko Park, Yang-Kyu Choi, and Kyung Cheol Choi, “Electro-Thermal Annealing (ETA) Method for Recovery of Cyclic Bending Stress in flexible a-IGZO TFTs”, Nanotechnology, Under review († equally contribution)

• Ui-Sik Jeong[†], Hagyoul Bae[†], **Choong-Ki Kim**, Dong-Il Moon, Myeong-Lok Seol, Sung-Jin Choi, and Yang-Kyu Choi, “Deformation of Spatial Trap Distribution in a Gate Oxide of Three-Dimensional FETs”, Solid State Electronics, Under review († equally contribution)

• Byeong-Woon Hwang, Hye-In Yeom, Daewon Kim, **Choong-Ki Kim**, Dongil Lee, and Yang-Kyu Choi, “Enhanced transconductance in a double-gate graphene field-effect transistor”, Electron Device Letter, *IEEE*, Under review

• Hagyoul Bae, **Choong-Ki Kim**, and Yang-Kyu Choi, “Extraction of Intrinsic Subgap Density-of-States in MoS_2 FETs using a Multi-Frequency Capacitance-Conductance Technique”, Applied Physics Letter, Under review

• Gun-Hee Kim, Yong-Yoon Kim, Hagyoul Bae, Jae Hur, **Choong-Ki Kim**, Tewook Bang, Yoon-Ik Son, Seong-Wan Ryu, and Yang-Kyu Choi, “A Novel Method for Separately Extracting of Source and Drain Resistance in MOSFETs with Verification”, Electron Device Letter, *IEEE*, Under review

• Hyun Jun Ahn, Jungmin Moon, Yujin Seo, Tae In Lee, **Choong-Ki Kim**, Hyun-Young Yu, Wan Sik Hwang,

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PATENTS

● 배학열, **김충기**, 최양규, “부유 기관구조를 갖는 MOS 트랜지스터에서 게이트 전압에 의존하는 반전전하층의 길이를 보정하여 정확한 진성 이동도를 추출하는 방법 및 장치”, 등록번호: 10-2015-0099657

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● 허재, 김다진, **김충기**, 방태욱, 최양규, “플라즈마 안테나 및 그의 제조 방법”, 출원번호: 10-2016-0013279

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REFERENCE

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